

714	724
Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

<i>SB</i> SCANNED <i>MT</i>	O.I.P.E.	PATENT DATE
	Q.A. <i>RC</i>	

APPLICATION NO. 09/670855	CONT/PRIOR	CLASS 714	SUBCLASS <i>724</i>	ART UNIT 2133	EXAMINER <i>Abraham Goncalves E Saad</i>
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APPLICANTS

Deterministic testing of edge-triggered logic
Computer

TITLE

PTO-2040
12/89

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
714	724	324	73.1		
INTERNATIONAL CLASSIFICATION		326	16		
G01R	31/28				
H03K	19/00				
G01R	27/28				
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets	Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	3		7	3	1	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<i>E Saad Abraham</i> 10/17/03 (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED <i>10/21/103</i>		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	<i>PHUNG M. CHUNG</i> PRIMARY EXAMINER (Primary Examiner)			ISSUE FEE		
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<i>V. Jones</i> 10/22/03 (Legal Instruments Examiner)			Amount Due <i>\$1330</i>	Date Paid	
ISSUE BATCH NUMBER						

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